

Member Meet

Discussion on the “Draft National Policy on Electronics (NPE)”
released by Department of E&IT in October 2011.

The NPE proposes several initiatives to reach an industry turnover of USD 400 billion. The policy also suggests that India should aim for global leadership in VLSI, chip design and other frontier technical areas. Innovation is the key to achieving these goals and becoming a global leader to reckon with.

Date : Friday, November 25 2011

Time : 10.30am to 1.30pm (to close with networking lunch)

Venue : Cadence Design Systems (I) Pvt. Ltd.
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