



Dear Patrons,

India's semiconductor ambition is no longer a vision—it's rapidly becoming a reality. The remarkable success of the **IESA Vision Summit 2026, held in the last week of February**, reflected the unprecedented momentum sweeping across the nation's semiconductor ecosystem. Uniting industry leaders, policymakers, startups, researchers, and academia under one roof, the summit sparked meaningful conversations, forged new collaborations, and celebrated innovation at every level.

Adding to this momentum, the inauguration of **Micron Technology's ATMP facility in Gujarat** by Hon. Prime Minister Shri Narendra Modi, and Hon. Chief Minister of Gujarat, Shri Bhupendrabhai Patel, marks a strategic leap in India's semiconductor manufacturing journey. This ₹22,516 crore project strengthens the nation's memory packaging and testing capabilities amid rising AI-driven demand, while reinforcing India's position as a trusted and globally competitive hub within the semiconductor supply chain.

India's technology ambitions took center stage at the **ET NOW Global Business Summit 2026** in New Delhi, where Hon. Prime Minister Shri Narendra Modi, Union Minister Shri Ashwini Vaishnaw, and prominent industry leaders shared their vision for the country's future. The discussions spotlighted India's rising leadership in semiconductors, AI, and digital technologies, reinforcing its emergence as a key driver of global innovation and technological advancement.

IESA continued to expand its global and industry engagement throughout the month. Mr. Ruchir Dixit, Chairman, IESA, and Mr. Sanjeev Keskar, Executive Council Member, met with **Mr. Kanishka Narayan, UK Minister for AI and Online Safety**, to discuss collaboration in AI, digital safety, and emerging technologies.

Across its **regional chapters**, IESA continued to drive industry engagement. Mr. Sudhir Naik, Head – Midwest Region, was invited as a guest speaker at a workshop on AI-Enabled Semiconductor Technologies for Quantum Computing, while the IESA North Chapter Meet in New Delhi brought together key industry and ecosystem stakeholders to discuss emerging opportunities in India's semiconductor sector.

The month concluded with the **IDSPS Industry Consortium and Advisory Board Meeting** at IIT Gandhinagar, where over 30 research projects in advanced packaging and integrated power electronics were showcased, further strengthening industry-academia collaboration and innovation.



Yours Sincerely
Ashok Chandak
President, IESA



Yours Sincerely
Ruchir Dixit
Chairman, IESA

IESA Media & PR Dashboard: February Highlights

Overall Media Reach (Metric Coverage)

Total Media Stories	625
Print	288
Online	328
TV Interview	9

Tier -1/ TV Media Visibility

Platform	Engagement
Deccan Herald	Exclusive Interaction
The Financial Express	Panel opportunity
The New Indian Express	Exclusive Panel Discussion

Policy & Industry Narrative

Topic	Platform
Union Budget-linked semiconductor industry story	Mint, Business Standard
India Semiconductor Mission 2.0	The Tribune and The Indian Express
India-US trade deal	The Economic Times, The Hindu Business Line and The New Indian Express.
HCL-Foxconn semiconductor	Dainik Bhaskar, Aaj, Pioneer, Voice of Lucknow, and Jansandesh Times

Regional Media Impact

Focus Area	Coverage
AI Summit narrative and semiconductor ecosystem	Business Remedies, Pioneer, DainikBhaskar, Samaya, Sakala and Orissa Time
HCL-Foxconn semiconductor	Dainik Bhaskar, Aaj, Pioneer, Voice of Lucknow, and Jansandesh Times
The quote on India AI Impact Summit	BusinessRemedies, Hukamnama Samachar, Evening Post, Dainik Bhaskar, and Pioneer
The "Chips, Compute and Clean Energy" theme	Samaya, Azad Sipahi, Sakala, and Orissa Times

Social Media Growth

Platform	Performance
LinkedIn	+1211 (Total 36,717)
Twitter/ X Engagement	5000+ Impressions, 467 Engagements and 49 new followers

Key Takeaway

IESA's media engagement significantly strengthened policy visibility, industry credibility, and national and regional reach reinforcing IESA as a trusted voice in India's ESDM and semiconductor ecosystem.

[View PR Coverage Summary](#)

ET NOW Global Business Summit 2026: India Charts a Bold Path to Semiconductor and Digital Leadership

At the ET NOW Global Business Summit 2026 in New Delhi, industry and policy leaders explored how India can turn disruption into long-term growth. Prime Minister Shri. Narendra Modi and Union Minister of Railways of India Shri. Ashwini Vaishnaw highlighted India's progress in digital governance, AI, and semiconductors. Representing the ESDM sector, Mr. Ashok Chandak, emphasized that India's semiconductor journey is a nation-building effort focused on ecosystem development, chip design talent, OSAT/ATMP capabilities, and manufacturing to position India as a global innovation and product powerhouse.



Micron ATMP Facility Inauguration Marks Major Boost to India's Semiconductor Push

India has taken a strategic leap in semiconductor manufacturing with the inauguration of Micron Technology's ATMP facility in Gujarat by Hon. Prime Minister Shri. Narendra Modi. and Hon. Chief Minister of Gujarat , Shri Bhupendrabhai Patel. Announced in 2026, the ₹22,516 crore project strengthens memory capabilities amid rising AI demand. Leaders like Ashok Chandak and Ajit Manocha, CEO and President SEMI. emphasised its role in boosting supply chain resilience and positioning India as a credible global semiconductor manufacturing hub.



UK Minister Engages with IESA for AI and Online Safety

Mr. Ruchir Dixit, Chairman – IESA, along with IESA EC Mr. Sanjeev Keskar, recently met with Mr. Kanishka Narayan, UK Minister for AI and Online Safety, at Taj, Bengaluru. The interaction provided an opportunity to exchange perspectives on emerging developments in artificial intelligence, digital safety, and technology collaboration. The discussion highlighted the importance of global partnerships in advancing innovation and strengthening engagement between industry stakeholders and international policymakers in the evolving technology landscape.



Highlights from Workshop on AI-Enabled Semiconductor Technologies for Quantum Computing

A one-day workshop on AI-Enabled Semiconductor Technologies for Quantum Computing was held on 3 February and hosted by ITM, bringing together participants to explore emerging intersections between AI, semiconductors, and quantum computing. Representing IESA, Mr. Sudhir Naik, Head – Midwest Region, attended as the guest speaker. He shared insights on evolving semiconductor technologies and the growing role of AI in accelerating quantum computing advancements, highlighting opportunities for industry collaboration and innovation within the expanding deep-tech ecosystem.



IESA Successfully Hosts North Chapter Meet 2026 in New Delhi

IESA successfully hosted the North Chapter Meet 2026 on 4 February in New Delhi, bringing together industry leaders, policymakers, startups, and ecosystem enablers to discuss the next phase of India's ESDM growth. The session opened with agenda setting by Sunny Malhotra, IESA North Region Chapter Head, and Mr. Ved Prakash Mall, Director – Sales & Marketing, IESA & SEMI. Mr. Ruchir Dixit, Chairperson – IESA and VP & Country Manager, Siemens EDA (Siemens Digital Industries Software), welcomed the gathering and encouraged stronger NCR participation in IESA's Core Interest Groups. Ashok Chandak, shared updates on the association's expanding engagement with government bodies and industry stakeholders.

Key highlights included:

- Vision Summit 2026 curtain raiser presented by Mr. Ashok Chandak and Mr. Ruchir Dixit
- Special addresses by Mr. Anand Kumar (STMicroelectronics), Mr. Vinod Sharma (ESSCI & Deki Electronics), Dr. Sanjay Kumar Gupta (STPI), and Mr. Desh Raj Bhadana (MEDEPC)
- Update on IESA reaching a 400-member base and expanding engagements shared by Mr. Ved Prakash Mall
- StartupMitra initiative update presented by Mr. Shankar Krishnamurthy
- Workforce and academic engagement initiatives outlined by Mr. Viswanathan K.G.

The meeting also featured an open house discussion with members, followed by closing remarks from Er. D. S. Yadav, Director – Government Relations & Policy, IESA



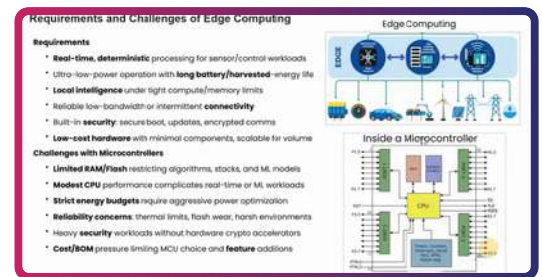
IDSPS Launches Industry Consortium at IIT Gandhinagar, Showcases 30+ Next-Gen Semiconductor Projects

The Indian Design, Semiconductors, Packaging and Systems (IDSPS) Program held its first industry consortium and Industry Advisory Board meeting at IIT Gandhinagar from 26–28 February. Led by Mr. Rajat Moona, Director, IITGN, the event brought together academia and industry to showcase 30+ research projects in advanced packaging and integrated power electronics. The initiative aims to build next-generation technologies and talent through collaboration, with upcoming co-development centres at IIT Gandhinagar and IIT Bhubaneswar.

AI on the Edge Webinar Draws Strong Participation from the Ecosystem

On 4 February 2026, IESA, in collaboration with NXP Semiconductors, hosted a technical webinar titled “AI on the Edge: Deploying TinyML on Microcontrollers.”

As part of the DeepTech Hackathon 2026, the session featured Mr. Mallikarjuna Bande, Director, NXP who shared insights on deploying TinyML for edge applications. The webinar saw participation from over 150 attendees and concluded with an engaging live Q&A, making it an insightful learning session for the community.



[Watch the Webinar](#)



Neural Network Optimization Session Highlights AI Efficiency for Edge Deployment

As part of the DeepTech Hackathon 2026 learning series, a knowledge session on “**Neural Network Optimization & Quantization**” was conducted on February 18, 2026, by Safer Afaque, Member of Technical Staff at GlobalFoundries, Introduced by Sourabh Ubale, the session focused on techniques to improve neural network performance while reducing computational and memory requirements.

Participants gained insights into quantization methods and optimization strategies that enable efficient AI deployment on semiconductor and edge devices. The session underscored optimization as an essential skill for AI engineers developing high-performance, resource-efficient models for real-world applications.

UPCOMING EVENTS

- Post Budget Webinar 2026 – 2027, 3rd March 2026
- Semiconductor Summit 2.0, 17–19 March 2026

StartUp / MSMEs of the Month - March 2026



Company Name: Fitron Manufacturing Tech Pvt Ltd

Location: Bengaluru

Founder: Bharath Raja Rao (Managing Director),
Tulasi Rao (Director)

Website: <https://www.fitronmantech.in/>

Email ID: bharathrrao@fitronmantech.in

Overview:

The company will focus on the indigenization of products for High Purity Industrial applications. Currently, nearly all products in this sector are imported into India, as domestic manufacturers have either failed or not attempted to meet the stringent quality standards required in the past.

Key Products/ Services/ Highlight:

- A dedicated effort will be made to meet or exceed international specifications by incorporating local materials, skilled labor, and innovative techniques in product development.
- The market potential is considerable, both for domestic consumption and, once developed, for global export.
- The objective is to align with government initiatives such as "Make in India" and "Viksit Bharat" by delivering high-quality products and solutions from India.
- Primarily, the industry is aimed at catering to semiconductor, Solar PV Cell, Biotech, and other high-purity application needs.
- These components are utilized in processes of chip making, where they navigate various hazardous gases into different phases of chip manufacturing. As these are going to carry hazardous gases, they need to be made with the highest quality standards and pass extensive testing.



Picotech Solutions

Company Name: Picotech Services India Private Limited

Location: Bengaluru

Website: <https://pico-tech.sg/>

Email ID: Venkat@pico-tech.sg

Overview:

Company Incorporated in 2017, providing Design and Build EPC contracts

Key Products/ Services/Features:

- Engineering Solutions and Consulting,
- Design & Build EPC Contracts,
- Specialized Facility & Cleanroom Construction,
- Total Gas and Chemical Solutions,
- Equipment provider and Total Facility Hookup,
- Project Management Services
- International Shipping and Distribution

IESA Offices

Bengaluru (Head Office):

India Electronics & Semiconductor Association
No.16, Kensington Road Cross, Next to RMZ
Millenia Tower, Ulsoor, Bangalore 560008
☎ 080 40939022, 080 40939918

Delhi:

Indo-French Chamber of Commerce & Industry (Delhi)
Gate no. 3, 1st floor, R K Khanna Tennis Stadium,
1, Africa Ave, near Deer Park, Safdarjung Enclave,
New Delhi, Delhi 110029, ☎ 080 40939022,
080 40939918

Ahmedabad:

2, Aryan Park Complex Nr,
Railway Crossing,
Thaltej - Shilaj Rd, Thaltej,
Ahmedabad, Gujarat 380054

www.iesasonline.org

SNAP REPORT

- The summit brought together policymakers, global technology leaders, startups, researchers, and academia to strengthen India's vision of becoming a global semiconductor hub
- Distinguished speakers included Shri Amitesh Kumar Sinha (India Semiconductor Mission), Shri Manish Chadha (Ministry of Commerce & Industry), Dr. Prof. Madan Mohan Tripathi (NIELIT), Mr. Rahul Sharanappa (Government of Karnataka), H.E. Ewout de Wit (Kingdom of the Netherlands), Ajit Manocha (SEMI), Gilroy Mathew (UST), Ruchir Dixit (IESA & Siemens EDA India), Ashok Chandak (IESA), and Prof. Dr. Rao Tummala
- Keynotes by Radhika Viswanathan, Gilroy Mathew, Rangesh Raghavan (Lam Research), and Tobias Helbig (NXP) highlighted the strategic importance of semiconductors, India's talent advantage, the role of R&D, and the need for responsible AI
- Talent development remained a central theme, with Prof. Vijay Raghunathan (Purdue University) and Ankush Wadhwa (BCG) emphasizing industry-academia collaboration and end-to-end capability building
- For the first time, IESA organised The DeepTech Hackathon 2026 along with GlobalFoundries India, NXP Semiconductors, and Inter Institutional Inclusive Innovations Center. It attracted 911 teams from 359 colleges. Chennai Institute of Technology emerged as the winner, followed by RAMCO Institute of Technology and IIT Indore
- Strategic partnerships announced during the summit included collaborations between Kaynes Semicon-Terminus Circuits, Akeana-Artrak Tech, RRP Electronics-HCLTech, and Suchi Semicon-HCLTech, aimed at strengthening India's semiconductor value chain
- International roundtables covering India-US, India-Singapore, India-EU, and India-Japan collaborations explored new opportunities for global partnerships, technology exchange, and ecosystem growth



Key Highlights

Participants
2000+

Exhibition Booths
100+

Skilling Workshop Attendees
300+

Country Roundtable Attendees
200+

Media Coverage
815

Startup
Pods **15** | Sessions **6**

Hackathon Teams
Registered **549** | Finalist **10**

Social Media Coverage
LinkedIn posts **61** | Tweets **107** | Impressions **450K+**



For the detailed report, contact: events@iesasonline.org