



Dear Patrons,

July reflected the growing momentum and maturity of India's semiconductor ecosystem, with developments spanning manufacturing, advanced packaging, policy, investment, workforce and international collaboration. Across the month, IESA continued to bring together industry leaders, policymakers, technology stakeholders and global partners to strengthen the foundations for India's next phase of semiconductor growth.

The progress of IESA member companies was particularly encouraging, with **CG SEMI Private Limited** and **ASIP Technologies** marking important milestones in OSAT and advanced System-in-Package capabilities. These developments highlight the increasing depth of India's semiconductor manufacturing and packaging ecosystem and the growing participation of Indian industry in building critical capabilities.

At the policy level, the approval of the **₹1.27 lakh crore Semicon India 2.0 Mission** marked an important step forward. **Ashok Chandak, President, IESA**, highlighted policy continuity as a key takeaway, while **Navin Bishnoi, Chairperson, IESA**, and **Pradeep Vajram, Vice Chairperson, IESA**, reaffirmed IESA's commitment to supporting implementation in partnership with MeitY, ISM and ecosystem stakeholders.

Industry-government dialogue remained strong through the **CEO Breakfast Meeting** with Karnataka Chief Minister Shri D. K. Shivakumar and the **Semiconductor Leadership Dialogue: From Vision to Execution**, featuring **Amitesh Kumar Sinha, CEO, ISM**, alongside IESA leadership and industry representatives. These conversations focused on translating India's semiconductor vision into sustained execution across the value chain.

International collaboration also remained in focus, with IESA engaging the **Consulate General of Canada** in Bengaluru, including Trade Commissioner Grace D'Souza, to explore opportunities with Canada's Semiconductor Council. Ashok Chandak also represented IESA at **NDTV's Indo-Japan Strategic Dialogue**, sharing perspectives on India-Japan semiconductor collaboration and resilient technology ecosystems.

Workforce development remained a strong priority through the NI Days India 2026 Industry Leadership Dialogue and the "AI Revolution Starts with Silicon" webinar, featuring Viswanathan K.G., Director - Workforce Development, IESA, and Namerita Khanna of NXP Semiconductors. The Startup Mitra webinar, led by Gaurav Asthana of Transjovan Capital, further explored M&A and growth capital for India's semiconductor startups.

Together, these initiatives reflect IESA's continued focus on building a connected ecosystem—one that brings policy, industry, technology, talent and global partnerships together to accelerate India's semiconductor ambitions.



Yours Sincerely
Ashok Chandak
President, IESA



Yours Sincerely
Navin Bishnoi
Chairman, IESA

IESA Media & PR Dashboard: July Highlights

Overall Media Reach (Metric Coverage)

Total Media Stories	445
Print	128
Online	305
Electronic & Radio	12

Tier -1/ TV Media Visibility

Platform	Engagement
CNBC TV18	Exclusive interviews and interactions on Semicon Mission 2.0
Zee Business, ET NOW, DD News,	Cabinet approval of Semicon Mission 2.0
Akashvani, All India Radio, Forbes India/News18	Exclusive interviews and video interaction

Policy & Industry Narrative

Topic	Platform
From Design to Silicon IP- The Complex Path of Owning Intellectual Property.	Financial Express
Discussions on Semiconductor Policies	Japanese trade publication AEI
Indian AI Firms Staring at a Structural Trap, Building on Open-Source Models That Will Soon Plateau.	The Print
India Eyes OSAT for Semicon Manufacturing Ecosystem.	The Hindu Business Line
India-Japan technology alliance	The Economic Times, Daily Hunt, DQ India, and Digital Terminal.

Regional Media Impact

Focus Area	Coverage
IESA's 3DGS Semicon groundbreaking	Fortune India and DQ India, The Assam Post, The Hills Times, The Meghalaya Guardian, Purvanchal Prahari, Samaya, Dharitri, Biswabani, Kalinga Mail, Orissa Times, Utkal Samaja, and Vishwavani.
Kaynes Semicon plant inauguration (Gujarat)	Outlook Business, DQ India, Digital Terminal, BusinessToday, Vicharavisthara, The Assam Post, The Hills Times, Sentinel, and Suvarna Times of Karnataka.
Tata-ASML partnership	DQ India, Digi Times, Silicon India, CIOL, Digital Terminal, PNI News, The North East Times, The Meghalaya Guardian, and The Assam Post.

Social Media Growth

Platform	Performance
LinkedIn	+513 New followers, 51,075 Impressions
Twitter/ X Engagement	516 Impressions

Key Takeaways

- IESA's media engagement significantly strengthened policy visibility, industry credibility, collaboration, and national and regional reach, reinforcing IESA and industry partners as a trusted voice in India's ESDM and semiconductor ecosystem.

[View PR Coverage Summary](#)



IESA Congratulates CG SEMI on Landmark OSAT Facility Inauguration

IESA congratulates its member company **CG SEMI Private Limited** on the inauguration of its OSAT facility in Sanand by Hon'ble Prime Minister Shri Narendra Modi. Developed in partnership with **CG Power and Industrial Solutions, Renesas Electronics, and Stars Microelectronics**, the facility marks a significant milestone in India's semiconductor manufacturing journey. **Vellayan Subbiah, Chairman, CG Power**, and **Malini Narayanamoorthi, Country Head & VP, Renesas Electronics India**, have played key roles in advancing this collaboration. IESA congratulates all stakeholders on this landmark achievement.

CEO Breakfast Meeting Explores Karnataka's Semiconductor and Technology Growth

The CEO Breakfast Meeting with Shri D. K. Shivakumar, Hon'ble Chief Minister of Karnataka, brought together government and industry leaders to discuss opportunities across semiconductors, electronics, AI and advanced technologies. Representing IESA, President **Ashok Chandak**, Vice Chairman **Pradeep Vajram**, **Hitesh Garg**, **Dr. Hemang Shah**, and Executive Council members shared industry perspectives on leveraging India's investment momentum, strengthening the semiconductor value chain, and advancing talent and innovation. Shri Priyank M. Kharge, Dr. Manjula and senior government officials also participated in the interaction.



IESA Congratulates ASIP Technologies on Advanced SiP Facility Milestone

IESA congratulates ASIP Technologies, a valued IESA member, on the foundation stone laying of its Advanced System-in-Package (SiP) facility in Visakhapatnam. Inaugurated by **Hon'ble Prime Minister Shri Narendra Modi**, the ₹2,387 crore project, in partnership with South Korea's AFACT Co. Ltd., will strengthen India's advanced semiconductor packaging capabilities. The ceremony was attended by Dharmendra Singh Yadav, Director – Government Relations & Policy, IESA, along with government officials, industry leaders and ecosystem partners.



IESA Welcomes ₹1.27 Lakh Crore Semicon India 2.0 Mission

IESA welcomed the Government of India's approval of the ₹1.27 lakh crore **Semicon India 2.0 Mission**, marking a significant step towards strengthening India's semiconductor ecosystem. Sharing his perspective, **Ashok Chandak, President, IESA**, highlighted that policy continuity is a key takeaway, with the expanded mission covering fabs, advanced packaging, design, R&D, talent, equipment and materials. **Navin Bishnoi, Chairperson, IESA**, and **Pradeep Vajram, Vice Chairperson, IESA**, reaffirmed IESA's commitment to working with MeitY, ISM and ecosystem stakeholders to accelerate implementation.

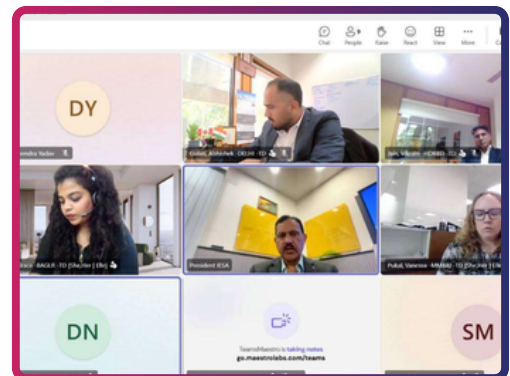


Spotlight on Semiconductor Leadership: From Vision to Execution

On **29 July 2026 in Bengaluru**, IESA hosted the **Semiconductor Leadership Dialogue: From Vision to Execution**, bringing together Shri Amitesh Kumar Sinha, CEO, India Semiconductor Mission (ISM), and senior industry leaders to discuss the implementation of SEMICON India 2.0. IESA leaders **Ashok Chandak, Navin Bishnoi, Pradeep Vajram, Vivek Tyagi, Srinivas Satya, Sanjeev Kesar, Gokul V. Subramaniam, Sundeept Gupta** and **Rajeev Ramamurthy**, among others, shared perspectives on strengthening India's semiconductor ecosystem across design, fabs, packaging, R&D and ecosystem development.

IESA Engages with Canadian Delegation to Explore Semiconductor Collaboration

IESA held a virtual discussion with the Consulate General of Canada in Bengaluru to explore opportunities for collaboration in the semiconductor sector. The discussion was attended by **Ashok Chandak, IESA President; Dharmendra Singh Yadav, IESA Director – Government Relations and Policy**; Grace D'Souza, Trade Commissioner, Consulate General of Canada in Bengaluru; and other members of the Canadian Consulate. The engagement explored potential collaboration with Canada's Semiconductor Council and opportunities to strengthen India-Canada partnerships across the semiconductor ecosystem.



[Watch the Full Discussion](#)

IESA President Ashok Chandak Joins NDTV Panel on India-Japan Semiconductor Partnership

Ashok Chandak, President, IESA, participated in the panel discussion **"Financing the Future: Investing in India"**, presented by **NDTV**, as part of the Indo-Japan Strategic Dialogue. The session brought together policymakers, diplomats, industry leaders and strategic experts to discuss India-Japan semiconductor partnerships, trusted supply chains, Japanese investments, economic security and resilient technology ecosystems. Sharing his perspective, Ashok Chandak highlighted the growing importance of the India-Japan partnership in semiconductor manufacturing, design, packaging, talent development and long-term technology collaboration.

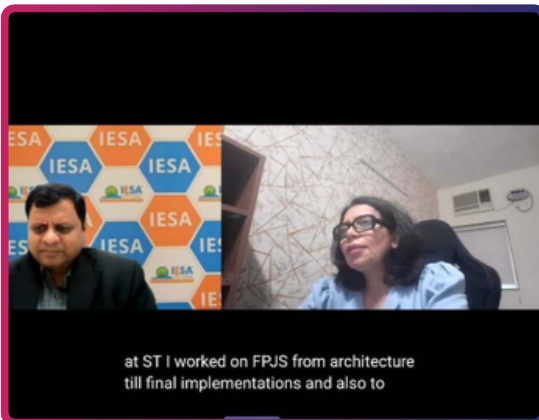
Highlights from the Industry Leadership Dialogue at NI Days India 2026

Viswanathan K.G., Director – Workforce Development, IESA, participated in the Industry Leadership Dialogue on **“The Evolution of Engineering Curriculum in India and the Influence of AI”** at **NI Days India 2026**. The panel brought together Dr. MJS Raman (CEO, IITM Pravartak), Dr. Debiprosad Roy Mahapatra (HAL Chair Professor, DoAero, IISc), Joseph Soo (Vice President, APAC Sales, NI), and other industry and academic leaders to discuss preparing India's engineering talent for an AI-driven future. Viswanathan highlighted IESA's workforce development initiatives and the importance of stronger academia-industry collaboration, hands-on learning and industry-ready talent to support India's semiconductor ambitions.



IESA Workforce Development Webinar Explores AI and Semiconductors

IESA conducted its **Workforce Development Webinar, “AI Revolution Starts with Silicon,”** on **23 July 2026**, with **193+ attendees** joining the session. **Mrs. Namerita Khanna, SoC Program Management Specialist, NXP Semiconductors,** shared insights on how semiconductor innovation is enabling the next wave of AI advancements. The session was moderated by **Vishwanathan K.G., Director – Workforce Development, IESA,** facilitating an engaging discussion on emerging semiconductor technologies and the evolving skills needed to support India's growing AI and semiconductor ecosystem.



Highlights from IESA Startup Mitra Webinar: M&A & Growth Capital in the Semiconductor Sector

The **IESA Startup Mitra Webinar** on **“M&A and Growth Capital Landscape in the Indian Semiconductor Sector”** brought together semiconductor startups and ecosystem stakeholders for an insightful session with **Mr. Gaurav Asthana, Managing Partner, Transjovan Capital.** With 236+ attendees, the session explored India's evolving semiconductor investment landscape, investor expectations, fundraising strategies, and emerging M&A trends across the ESDM ecosystem.



StartUp / MSMEs of the Month - July 2026



Company Name: VSORA

Location: HQ – Paris France

Founder :

Khaled Maalej : Co-founders & CEO

Pierre-Emmanuel Bernard : Co-founders- Head Of ASICs

Julien Schmitt : Co-founders – Head of Compilation

Trung Dung Nguyen : Co-founders- Head of AI & Algorithms

Website: <https://vsora.com/>

Email ID: amit.rao@vsora.com
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Overview:

VSORA is a French fabless semiconductor company founded in 2015, developing a complete AI inference solution for data centers, from silicon to software and systems. Headquartered near Paris and operating across eight regions, including India, VSORA brings more than 25 years of high-performance DSP and AI engineering expertise to a new generation of inference-first accelerators. Our architecture is designed to address the memory wall and improve the economics of AI deployment through higher efficiency, lower cost per token, deterministic low latency and an open, CUDA-free software environment.

By joining IESA, VSORA aims to deepen collaboration with India's semiconductor, cloud, data-center and engineering ecosystem, contributing technology and expertise to support the development of sustainable AI data centres and the continued growth of the country's ESDM and AI infrastructure landscape

Key Products/ Services/ Highlight:

- Jotunn8 is VSORA's data-center AI accelerator, purpose-built for inference. Its chiplet-based architecture combines 3,200 TFLOPS of dense FP8 compute, 288 GB of on-package HBM3e and 8 TB/s of memory bandwidth, enabling large models, long context windows and multi-model or agentic workloads.
- Unlike general-purpose GPU architectures, Jotunn8 is designed to minimize data movement and maintain high compute utilization, delivering predictable low-latency performance, higher performance per watt and a lower cost per token.
- Fully programmable, CUDA-free and compatible with standard AI frameworks including PyTorch and ONNX, Jotunn8 is designed for straightforward integration into existing environments. For India's expanding AI infrastructure ecosystem, it offers cloud providers, OEM/ODMs, data-center operators and enterprises a scalable platform for efficient, high-density inference and the development of more sustainable AI data centres.

StartUp / MSMEs of the Month - July 2026

Altium

Company Name: Altium Software India Pvt Ltd

Location: Unit 704A, 7th Floor, World Trade Center Brigade Gateway Campus, 26/1, Dr. Rajkumar Road Malleswaram-Rajajinagar, Bengaluru, Karnataka 560050

Website: www.altium.com/in

Email ID: sreekanth.tammana@altium.com

Overview:

Altium Limited, part of the Renesas Group, is a global leader in software and solutions for electronics design, product development, and lifecycle management. Since 1985, Altium has empowered engineers and organizations to design, develop, and deliver innovative electronic products faster and efficiently. Altium is leading the digital transformation of electronics product development by connecting design, supply chain, manufacturing, and lifecycle workflows within a unified digital ecosystem. Our solutions enable distributed teams to collaborate securely, manage complexity, and accelerate time to market across every stage of the creation process.

Key Products/ Services/ Highlights:

Altium Designer®: the industry-leading PCB design software platform Altium 365®: a secure cloud-based collaboration and data management platform Altium Develop™: a solution for individual engineers and small teams to design, share, and stay in sync without heavy administrative overhead

Altium Agile™ (Teams and Enterprise): platform-based solutions for electronic product and systems development, designed to deliver control without adding complexity Octopart Discover™: brings design intent and persistent system context into solutions discovery, moving engineers beyond part search to discovering and evaluating system-fit solutions Octopart®: a leading search engine and intelligence platform for electronic components.

Together, these solutions create a connected digital thread across design, development, sourcing, and lifecycle management — enabling organizations to improve productivity, reduce risk, manage component availability, and deliver increasingly complex, smart, and connected products. With a relentless focus on usability, integration, security, and real-time supply chain intelligence, Altium supports global organizations in navigating high-speed design challenges, power and packaging constraints, compliance requirements, and rapidly evolving electronics markets.

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